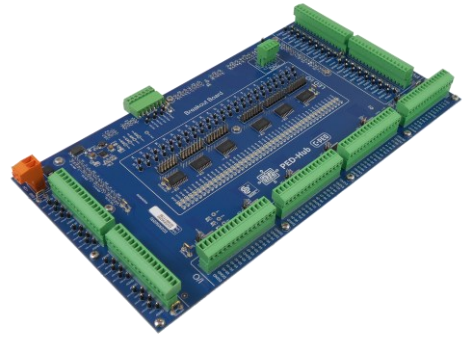


Breakout Board

for PED-Board
PN: ED250502R0

Key Technical Features:

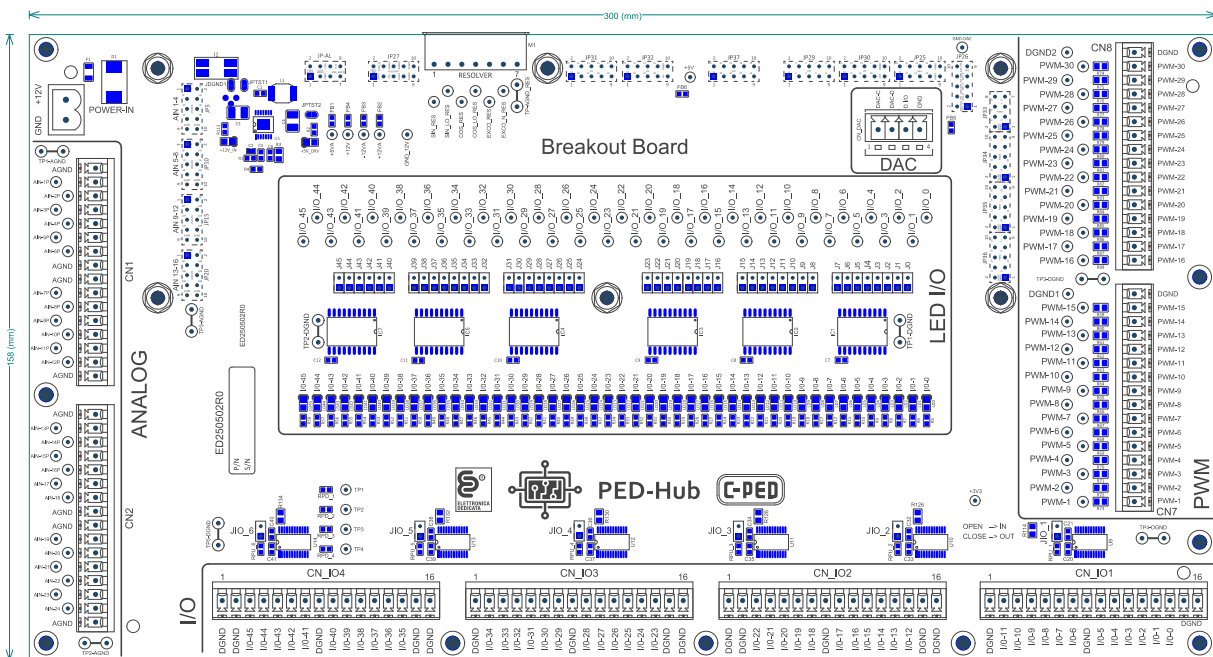
- All PED-Board inputs/outputs available through connectors!
- LabVIEW Starting Project with all signals mapped!




Characteristics

Storage temperature (IEC 60068-2-1, IEC 60068-2-2)	-40 to 85 °C
Operating temperature	-25 to 60 °C
Operating humidity (IEC 60068-2-56)	10 to 90% RH, noncondensing
Storage humidity (IEC 60068-2-56)	5 to 95% RH, noncondensing
Maximum altitude	5000 m
Pollution Degree (IEC 60664)	2
Dimensions	300mm x 158mm

Layout

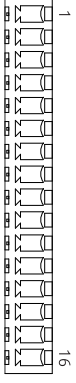


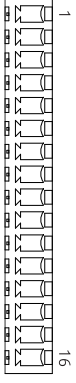
Power Supply

Connector	Pin	Input
 2P	1	+12V
	2	GND

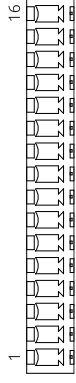


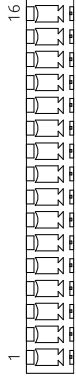
Mapping – Analog

PED-Board	Test Point Label	Connector	
		 CN1	Pin
AGND	AGND		1
AIN-1P	AIN-1P		2
AIN-2P	AIN-2P		3
AIN-3P	AIN-3P		4
AIN-4P	AIN-4P		5
AIN-5P	AIN-5P		6
AIN-6P	AIN-6P		7
AGND	AGND		8
AGND	AGND		9
AIN-7P	AIN-7P		10
AIN-8P	AIN-8P		11
AIN-9P	AIN-9P		12
AIN-10P	AIN-10P		13
AIN-11P	AIN-11P		14
AIN-12P	AIN-12P		15
AGND	AGND	16	

PED-Board	Test Point Label	Connector	
		 CN2	Pin
AGND	AGND		1
AIN-13P	AIN-13P		2
AIN-14P	AIN-14P		3
AIN-15P	AIN-15P		4
AIN-16P	AIN-16P		5
AIN-17	AIN-17		6
AIN-18	AIN-18		7
AGND	AGND		8
AGND	AGND		9
AIN-19	AIN-19		10
AIN-20	AIN-20		11
AIN-21	AIN-21		12
AIN-22	AIN-22		13
AIN-23	AIN-23		14
AIN-24	AIN-24		15
AGND	AGND	16	

Mapping – PWM

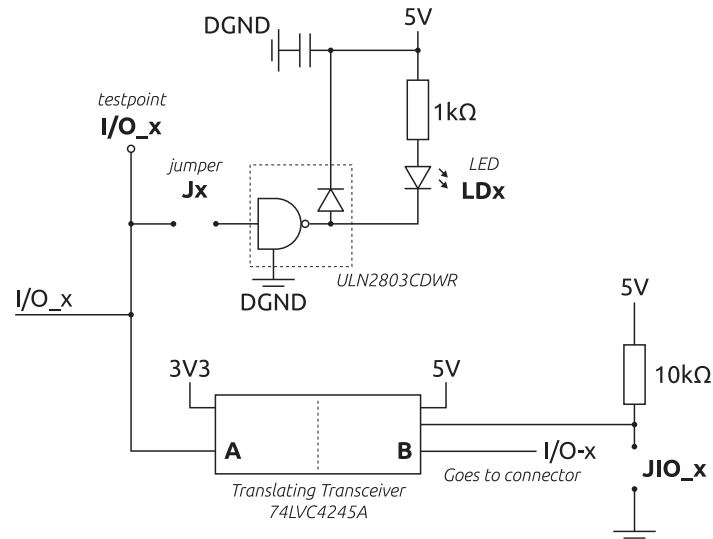
PED-Board	Test Point Label	Connector	
		 CN7	Pin
PWM-1	PWM-1		1
PWM-2	PWM-2		2
PWM-3	PWM-3		3
PWM-4	PWM-4		4
PWM-5	PWM-5		5
PWM-6	PWM-6		6
PWM-7	PWM-7		7
PWM-8	PWM-8		8
PWM-9	PWM-9		9
PWM-10	PWM-10		10
PWM-11	PWM-11		11
PWM-12	PWM-12		12
PWM-13	PWM-13		13
PWM-14	PWM-14		14
PWM-15	PWM-15		15
DGND	DGND	16	

PED-Board	Test Point Label	Connector	
		 CN8	Pin
PWM-16	PWM-16		1
PWM-17	PWM-17		2
PWM-18	PWM-18		3
PWM-19	PWM-19		4
PWM-20	PWM-20		5
PWM-21	PWM-21		6
PWM-22	PWM-22		7
PWM-23	PWM-23		8
PWM-24	PWM-24		9
PWM-25	PWM-25		10
PWM-26	PWM-26		11
PWM-27	PWM-27		12
PWM-28	PWM-28		13
PWM-29	PWM-29		14
PWM-30	PWM-30		15
DGND	DGND	16	

PWM PED-Board signals to Break-out connector connected by a 47R resistor



Mapping - I/Os - Circuits

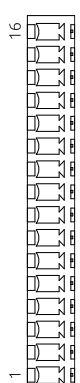


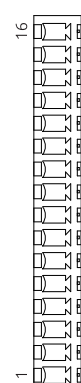
PED-Board (3V3) A	Connector (5V) B	JIO_x	Direction Selection		
I/O_0 ... I/O_7	I/O-0 ... I/O-7	JIO_1			
I/O_8 ... I/O_15	I/O-8 ... I/O-15	JIO_2			
I/O_16 ... I/O_23	I/O-16 ... I/O-23	JIO_3			
I/O_24 ... I/O_31	I/O-24 ... I/O-31	JIO_4			
I/O_32 ... I/O_39	I/O-32 ... I/O-39	JIO_5			
I/O_40 ... I/O_45	I/O-40 ... I/O-45	JIO_6			
			Jumper	A	B
			CLOSE	A=B	IN
			OPEN	IN	B=A

PED-Board (3V3)	Test Point Label	Jx	LDx	Translating Transceiver B (5V)	Connector	
DGND	DGND	-	-	-		Pin
DGND	DGND	-	-	-		16
DGND	DGND	-	-	-		15
I/O_0	I/O_0	J0	LD0	I/O-0		14
I/O_1	I/O_1	J1	LD1	I/O-1		13
I/O_2	I/O_2	J2	LD2	I/O-2		12
I/O_3	I/O_3	J3	LD3	I/O-3		11
I/O_4	I/O_4	J4	LD4	I/O-4		10
I/O_5	I/O_5	J5	LD5	I/O-5		9
DGND	DGND	-	-	-		8
I/O_6	I/O_6	J6	LD6	I/O-6		7
I/O_7	I/O_7	J7	LD7	I/O-7		6
I/O_8	I/O_8	J8	LD8	I/O-8		5
I/O_9	I/O_9	J9	LD9	I/O-9		4
I/O_10	I/O_10	J10	LD10	I/O-10		3
I/O_11	I/O_11	J11	LD11	I/O-11		2
DGND	DGND	-	-	-	1	

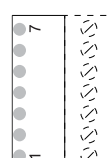
PED-Board (3V3)	Test Point Label	Jx	LDx	Translating Transceiver B (5V)	Connector	
DGND	DGND	-	-	-		Pin
DGND	DGND	-	-	-		16
DGND	DGND	-	-	-		15
I/O_12	I/O_12	J12	LD12	I/O-12		14
I/O_13	I/O_13	J13	LD13	I/O-13		13
I/O_14	I/O_14	J14	LD14	I/O-14		12
I/O_15	I/O_15	J15	LD15	I/O-15		11
I/O_16	I/O_16	J16	LD16	I/O-16		10
I/O_17	I/O_17	J17	LD17	I/O-17		9
DGND	DGND	-	-	-		8
I/O_18	I/O_18	J18	LD18	I/O-18		7
I/O_19	I/O_19	J19	LD19	I/O-19		6
I/O_20	I/O_20	J20	LD20	I/O-20		5
I/O_21	I/O_21	J21	LD21	I/O-21		4
I/O_22	I/O_22	J22	LD22	I/O-22		3
DGND	DGND	-	-	-		2
DGND	DGND	-	-	-	1	



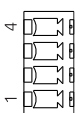
PED-Board (3V3)	Test Point Label	Jx	LDx	Translating Transceiver B (5V)	Connector	
					CN-IO3	Pin
DGND	DGND	-	-	-		
DGND	DGND	-	-	-	15	
I/O_23	I/O_23	J23	LD23	I/O-23	14	
I/O_24	I/O_24	J24	LD24	I/O-24	13	
I/O_25	I/O_25	J25	LD25	I/O-25	12	
I/O_26	I/O_26	J26	LD26	I/O-26	11	
I/O_27	I/O_27	J27	LD27	I/O-27	10	
I/O_28	I/O_28	J28	LD28	I/O-28	9	
DGND	DGND	-	-	-	8	
I/O_29	I/O_29	J29	LD29	I/O-29	7	
I/O_30	I/O_30	J30	LD30	I/O-30	6	
I/O_31	I/O_31	J31	LD31	I/O-31	5	
I/O_32	I/O_32	J32	LD32	I/O-32	4	
I/O_33	I/O_33	J33	LD33	I/O-33	3	
I/O_34	I/O_34	J34	LD34	I/O-34	2	
DGND	DGND	-	-	-	1	

PED-Board (3V3)	Test Point Label	Jx	LDx	Translating Transceiver B (5V)	Connector	
					CN-IO4	Pin
DGND	DGND	-	-	-		
DGND	DGND	-	-	-	15	
I/O_35	I/O_35	J35	LD35	I/O-35	14	
I/O_36	I/O_36	J36	LD36	I/O-36	13	
I/O_37	I/O_37	J37	LD37	I/O-37	12	
I/O_38	I/O_38	J38	LD38	I/O-38	11	
I/O_39	I/O_39	J39	LD39	I/O-39	10	
I/O_40	I/O_40	J40	LD40	I/O-40	9	
DGND	DGND	-	-	-	8	
I/O_41	I/O_41	J41	LD41	I/O-41	7	
I/O_42	I/O_42	J42	LD42	I/O-42	6	
I/O_43	I/O_43	J43	LD43	I/O-43	5	
I/O_44	I/O_44	J44	LD44	I/O-44	4	
I/O_45	I/O_45	J45	LD45	I/O-45	3	
DGND	DGND	-	-	-	2	
DGND	DGND	-	-	-	1	

Resolver

PED-Board	Test Point Label	Connector	
		M1	Pin
SIN_RESOLVER	SIN_RES		1
SINLO_RESOLVER	SINLO_RES		2
COS_RESOLVER	COS_RES		3
COSLO_RESOLVER	COSLO_RES		4
EXCO_RESOLVER	EXCO_RES		5
EXCO_n_RESOLVER	EXCO_n_RES		6
AGND_RES	TP_AGND_RES		7

DAC

PED-Board	Test Point Label	Connector	
		CN_DAC	Pin
DAC_C	-		1
DAC_D	-		2
DIO_ISO	-		3
GND_DAC_ISO	GND_DAC		4

